March 2010



FGA20S120M 1200V, 20A Shorted-Anode IGBT

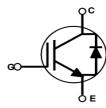
Features

- · High speed switching
- Low saturation voltage: V_{CE(sat)} =1.55V @ I_C = 20A
- High input impedance
- RoHS compliant

Applications

- Induction heating and Microvewave Oven
- Soft switching Application





Using advanced Field Stop Trench and shorted-anode technology, Fairchild's 1200V Shorted-Anode Trench IGBTs offer supe-

rior conduction and switching performances, and easy parallel

operation with exceptional avalanche capability. This device is

designed for Induction heating Microvewave Oven.

General Description

Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Description		Ratings	Units	
V _{CES}	Collector to Emitter Voltage		1200	V	
V _{GES}	Gate to Emitter Voltage		±25	V	
I _C	Collector Current	@ T _C = 25°C	40	А	
	Collector Current	@ T _C = 100 ^o C	20	A	
I _{CM (1)}	Pulsed Collector Current		60	A	
I _F	Diode Continuous Forward Current	@ T _C = 25°C	40	A	
I _F	Diode Continuous Forward Current	@ T _C = 100 ^o C	20	A	
P _D	Maximum Power Dissipation	@ T _C = 25°C	348	W	
	Maximum Power Dissipation	@ T _C = 100 ^o C	174	W	
TJ	Operating Junction Temperature		-55 to +175	°C	
T _{stg}	Storage Temperature Range		-55 to +175	°C	
TL	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C	

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Units
$R_{\theta JC}$ (IGBT)	Thermal Resistance, Junction to Case		0.43	°C/W
$R_{\theta JC}(Diode)$	Thermal Resistance, Junction to Case		0.43	°C/W
R_{\thetaJA}	Thermal Resistance, Junction to Ambient		40	°C/W

Notes:

1: Limited by Tjmax

•		Package	ackage Reel Size		Tape Width		Quantity 30	
		TO-3PN						
Electric	al Char	acteristics of the		5°C unless otherwise noted	I			
Symbol			-	Test Conditions		Тур.	Max.	Units
Off Charac	teristics							
BV _{CES}	Collector to	Emitter Breakdown Voltag	e V _{GE} = 0V, I _C	= 2mA	1200	-	-	V
I _{CES}	Collector C	ut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$		-	-	1	mA
I _{GES}	G-E Leakage Current		$V_{GE} = V_{GES}$	$V_{GE} = V_{GES}, V_{CE} = 0V$		-	±250	nA
On Charac	toristics		ш					
V _{GE(th)}		nold Voltage	I _C = 20mA, V		4.5	6.0	7.5	V
	GE(th) G-E Threshold Voltage		$I_{\rm C} = 20$ A, $V_{\rm GE} = 15$ V		-	1.55	1.85	V
V _{CE(sat)}	Collector to Emitter Saturation Voltage		$l_{c} = 20A V_{c}$		-	1.75	-	V
				_E = 15V,	-	1.85	-	V
V _{FM} Diode Forward Voltage		I _F = 20A, T _C	= 25°C		1.7	2.2	V	
			I _F = 20A, T _C	= 175 [°] C		2.1	-	v
Dynamic C C _{ies} C _{oes}	Input Capa Output Cap	citance	V _{CE} = 30V, V	/ _{GE} = 0V,		2680 53		pF pF
			V _{CE} = 30V, V	/ _{GE} = 0V,				
C _{res}		Reverse Transfer Capacitance		f = 1MHz		43		pF
	<u> </u>				ļ			
Switching (Turn-On D				-	43	-	ns
t _{d(on)} t _r	Rise Time		_			176	-	ns
t _{d(off)}	Turn-Off D	elav Time	V _{CC} = 600V,	L. = 20A	_	310	-	ns
t _f	Fall Time		R _G = 10Ω, V		_	320	480	ns
E _{on}		witching Loss	Resistive Lo	ad, T _C = 25°C	-	0.52	-	mJ
E _{off}		witching Loss	-		-	1.43	2.145	mJ
E _{ts}	Total Switc	0	-		-	1.95	-	mJ
t _{d(on)}	Turn-On D	<u> </u>			-	41	-	ns
t _r	Rise Time	-			-	260	-	ns
t _{d(off)}	Turn-Off D	elay Time	V _{CC} = 600V,	I _C = 20A,	-	345	-	ns
t _f	Fall Time		R _G = 10Ω, V	′ _{GE} = 15V,	-	520	-	ns
E _{on}	Turn-On S	witching Loss	Resistive Lo	sistive Load, T _C = 175 ^o C	-	0.78	-	mJ
E _{off}	Turn-Off S	witching Loss	1		-	1.97	-	mJ
E _{ts}	Total Switc	hing Loss			-	2.75	-	mJ
Qg	Total Gate	Charge			-	210	-	nC
Q _{ge}	Gate to En	nitter Charge	$V_{CE} = 600V,$	I _C = 20A,	-	18	-	nC
0		llector Charge	V _{GE} = 15V			119	-	nC

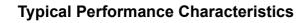
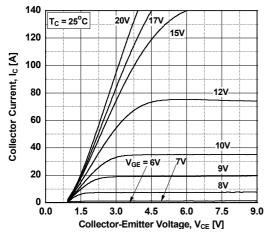
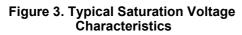


Figure 1. Typical Output Characteristics





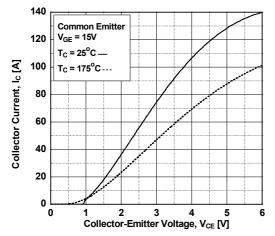


Figure 5. Saturation Voltage vs. Case Temperature at Variant Current Level

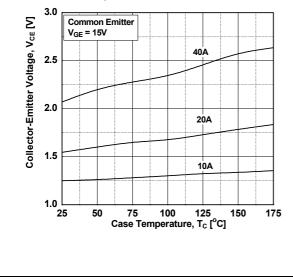


Figure 2. Typical Output Characteristics

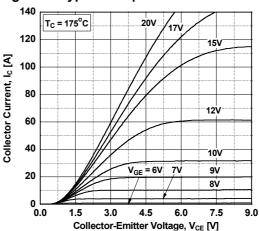


Figure 4. Transfer Characteristics

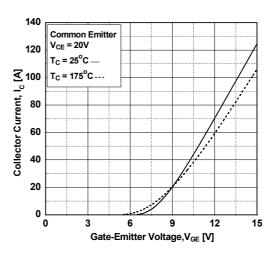
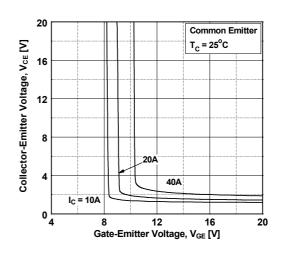


Figure 6. Saturation Voltage vs. VGE



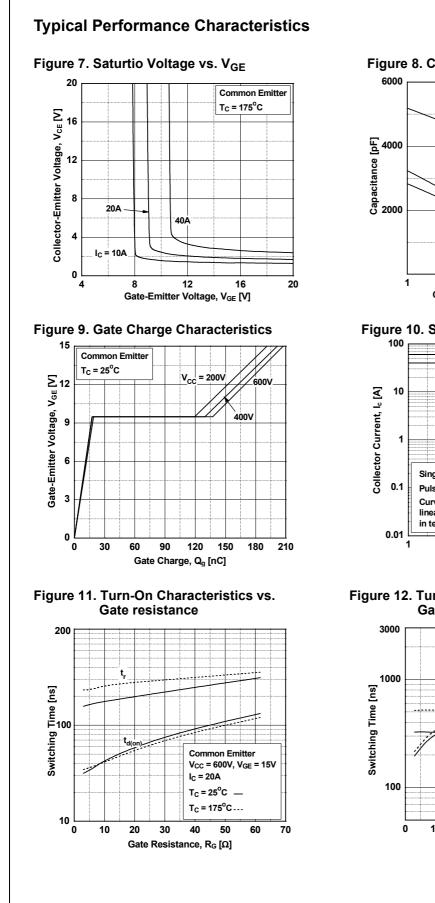
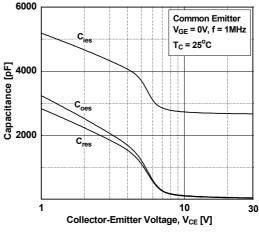


Figure 8. Capacitance Characteristics





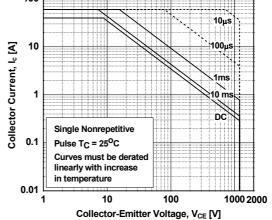
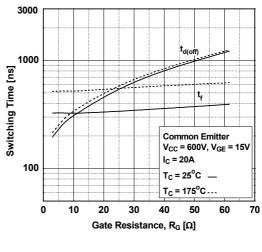
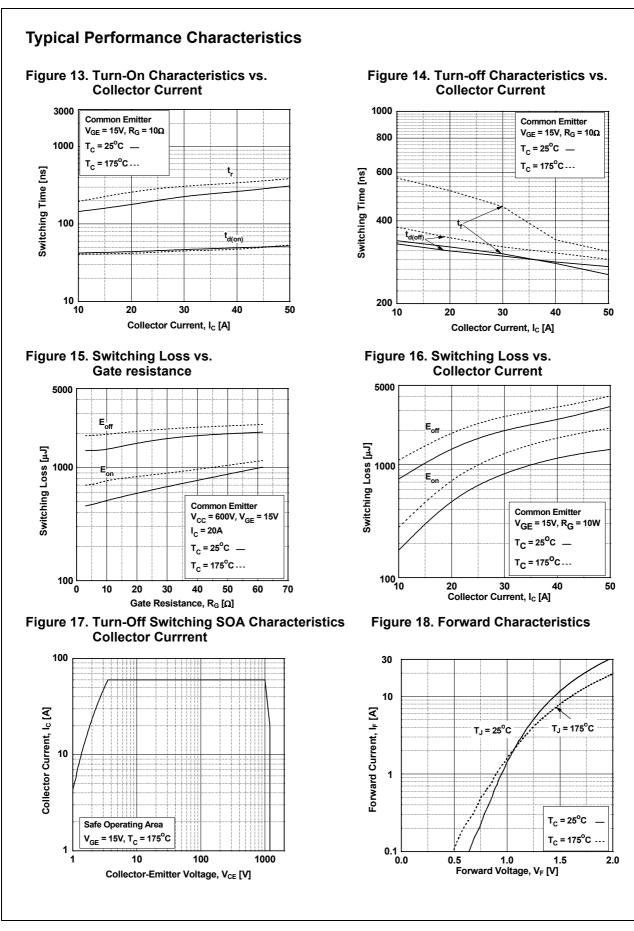
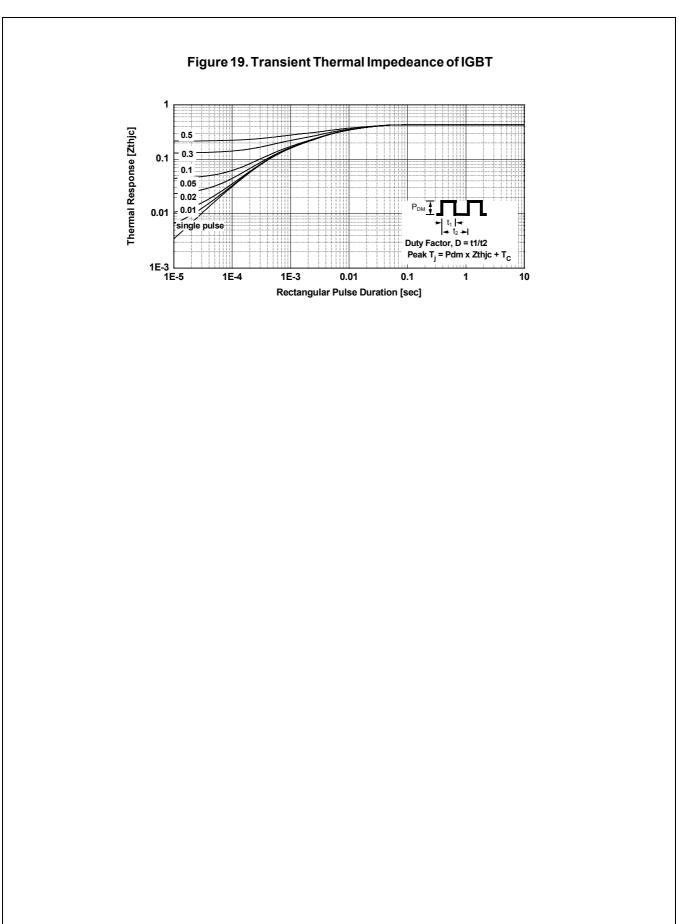


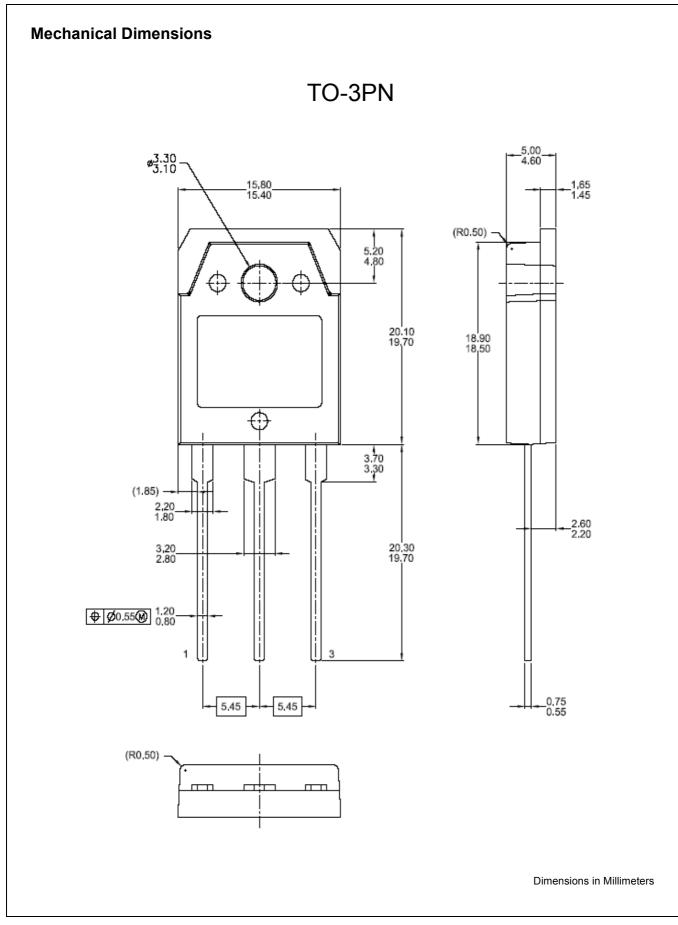
Figure 12. Turn-Off Characteristics vs. Gate resistance



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